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UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,828,654 B2  
DATED : December 7, 2004  
INVENTOR(S) : Derek Tam et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 6,

Line 55, "the said gate electrode and coupled to a second" should appear as -- said gate electrode and coupled to a second --.

Line 61, "The circuit of claim 1, wherein said P+ gate-to-substrate" should appear as -- The circuit of claim 1, wherein the gate-to-substrate --.

Column 7,

Line 5, "circuit additionally includes oscillator, a reference frequency" should appear as -- circuit additionally includes an oscillator, a reference frequency --.

Signed and Sealed this

Nineteenth Day of April, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" for "Dudas".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*